1

2

3

1

1

2

3

4

CLAIMS

NED EXECUTIVE STRUCTURE FOR REA

What is claimed is:

A method for repetitively executing a plurality of software packages at one or more rates, utilizing a common set of computational resources, the method comprising the steps: generating a sequence of time intervals for each of the plurality of software

- packages, the time intervals belonging to one software package not overlapping the time intervals
- belonging to any other of the plurality of software packages; 5
 - executing a plurality of software packages, each software package being executed during the time intervals of its sequence of time intervals.
 - 2. The method of claim 1\ wherein the plurality of software packages of the "executing" step includes only valid software packages, the method further comprising the step: utilizing one or more tests to identify the software packages that are valid.
 - 3. The method of claim 2 wherein one of the tests for validity is a one's complement checksum test of a software package's program memory.
 - 4. The method of claim 2 wherein a software package is assigned its own dedicated memory region, one of the tests for validity being whether the address returned for the software package's initialization procedure lies within its dedicated memory region.
- 5. The method of claim 4 wherein one of the tests is whether the address is returned 2 within a predetermined time.
 - 6. The method of claim 2 wherein a software package is assigned its own dedicated memory region, the software package's dedicated memory region including a stack memory

- 3 region and/or a heap memory region, one of the tests for validity being whether the stack memory
- 4 range and/or the heap memory range assigned during the execution of the software package's
- 5 initialization procedure and the various associated entry points lies within the software package's
- 6 dedicated memory region.
- The method of claim 6 wherein one of the tests is whether the stack memory range
- 2 and/or the heap memory range and the various associated entry points are returned within a
- 3 predetermined time.
 - 8. The method of claim 1 wherein a software package is assigned its own dedicated
- 2 memory region.
- 1 9. The method of claim 8 wherein the software package's dedicated memory region
 - includes a stack memory region, a software package's stack residing in the software package's
- 3 stack memory region.
- 1 10. The method of claim 1 wherein a software package includes background tasks as
- well as foreground tasks, the background tasks being performed after the foreground tasks have
- 3 been completed.
- 1 11. The method of claim 10 wherein a background task is an infinite loop.
- 1 12. The method of claim 10 wherein the software package causes the power utilized
- 2 in executing the software package to be minimized after completion of the background tasks.
- 1 13. The method of claim 1 wherein a failure in the execution of a software package
- 2 causes information to be logged in a failure log.
- 1 14. The method of claim 13 wherein a failure in execution is linked to the software
- 2 package that caused the failure.

2

3

4

5

6

1

2

1

2

3

4

5

1

2

3

- package is represented by one or more performance-quality parameters, values of the one or more performance-quality parameters being determined from the information logged in a failure log, the execution of a software package being subject to a plurality of execution options, an execution option being selected on the basis of one or more performance-quality parameter values.
- 16. The method of claim 15 wherein the plurality of execution options are user configurable.
- 17. The method of claim 15 wherein performance-quality parameters include the number of failures and/or the rate of failures for one or more classes of failures recorded in a software package's failure log.
- 18. The method of claim 1 wherein safety-critical software is placed in one or more separate partitions thereby isolating the safety-critical software from non-safety-critical software.
- 19. The method of claim 1 wherein each of the plurality of software packages is assigned its own memory block, a software package being enabled to read data only from zero or more memory blocks associated with other software packages, the zero or more memory blocks readable by a software package being either predetermined or determined during execution of the software packages in accordance with a set of one or more rules.
- 20. The method of claim 1 wherein each of the plurality of software packages is assigned its own memory block, a software package being enabled to write data only to zero or more memory blocks associated with other software packages, the zero or more memory blocks

03/27/01 16

Į	1
Ļ	7
Ę	Ţ
ľ	Ш
ł	÷
Pur.	Ī
1	Ų
÷	4
≅	
111.17	H1
fluit mil	Ų
that and the	firm time
that mad than that	4.74 4.7.1 4.7.1
that mad than that	firm time
that and thus ball that	4.74 4.7.1 4.7.1

3

4

5

1

2

1

2

1

1

- writeable by a software package being either predetermined or determined during execution of
 the software packages in accordance with a set of one or more rules.

 The method of claim 1 wherein an executive software package enforces the
 - discipline that each software package executes only during the time intervals of its sequence of time intervals, the executive software package determining when the execution of a software package extends into a time interval belonging to the sequence of time intervals assigned to another software package and performs a remedial action.
 - 22. The method of claim 1 wherein the presence of those software packages that are present is detected.
 - 23. The method of claim 1 wherein one or more of the plurality of software packages are independently compiled, linked, and loaded.
 - 24. The method of claim 1 wherein a software package has its own stack, the software package's stack being selected prior to executing the software package.
 - 25. Apparatus for practicing the method of claim 1.
- 1 26. Apparatus for repetitively executing a plurality of software packages at a plurality of rates, the apparatus comprising:
- a means for generating a sequence of time intervals for each of the plurality of software packages, the time intervals belonging to one software package not overlapping the time intervals belonging to any other of the plurality of software packages;
- a means for executing a plurality of software packages, each software package

 being executed during the time intervals of its sequence of time intervals.

1

2

3

- 27. 1 The apparatus of claim 26 wherein the plurality of software packages executed by the "executing" means includes only valid software packages, the apparatus further comprising: 2 a means for utilizing one or more tests to identify the software packages that are 3 valid. 4 28. The apparatus of claim 27 wherein one of the tests for validity is a one's 1 2 complement checksum test of a software package's program memory. 29. The apparatus of claim 27 wherein a software package is assigned its own 1 dedicated memory region, one of the tests for validity being whether the address returned for the 2 software package's initialization procedure lies within its dedicated memory region. 30. The apparatus of claim 29 wherein one of the tests is whether the address is returned within a predetermined time. 31. The apparatus of claim 27 wherein a software package is assigned its own 1 dedicated memory region, the software package's dedicated memory region including a stack memory region and/or a heap memory region, one of the tests for validity being whether the stack memory range and/or the heap memory range assigned during the execution of the software 4 package's initialization procedure and the various associated entry points lies within the software 5
 - 32. The apparatus of claim 31 wherein one of the tests is whether the stack memory range and/or the heap memory range and the various associated entry points are returned within a predetermined time.

03/27/01 18

package's dedicated memory region.

6

values.

1	33.	The apparatus of claim 26 wherein a software package is assigned its own
2	dedicated memory region.	
1	34.	The apparatus of claim 33 wherein the software package's dedicated memory
2	region includes a stack memory region, a software package's stack residing in the software	
3	package's stack memory region.	
1	35.	The apparatus of claim 26 wherein a software package includes background tasks
2	as well as fo	oreground tasks, the background tasks being performed after the foreground tasks
3	have been co	mpleted.
1	36.	The apparatus of claim 35 wherein a background task is an infinite loop.
1	37.	The apparatus of claim 35 wherein the software package causes the power utilized
2	in executing	the software package to be minimized after completion of the background tasks.
1	38.	The apparatus of claim 26 wherein a failure in the execution of a software
2	package caus	es information to be logged in a failure log.
1	39.	The apparatus of claim 38 wherein a failure in execution is linked to the software
2	package that	caused the failure.
1	40.	The apparatus of claim 38 wherein quality of performance in executing a software
2	package is re	presented by one or more performance-quality parameters, values of the one or more
3	performance-	quality parameters being determined from the information logged in a failure log,
4	the execution	n of a software package being subject to a plurality of execution options, an

execution option being selected on the basis of one or more performance-quality parameter

3

4

5

1

2

3

1

2

- 1 41. The apparatus of claim 40 wherein the plurality of execution options are user 2 configurable.
- 1 42. The apparatus of claim 40 wherein performance-quality parameters include the number of failures and/or the rate of failures for one or more classes of failures recorded in a software package's failure log.
 - 43. The apparatus of claim 26 wherein safety-critical software is placed in one or more separate partitions thereby isolating the safety-critical software from non-safety-critical software.
 - 44. The apparatus of claim 26 wherein each of the plurality of software packages is assigned its own memory block, a software package being enabled to read data only from zero or more memory blocks associated with other software packages, the zero or more memory blocks readable by a software package being either predetermined or determined during execution of the software packages in accordance with a set of one or more rules.
 - 45. The apparatus of claim 26 wherein each of the plurality of software packages is assigned its own memory block, a software package being enabled to write data only to zero or more memory blocks associated with other software packages, the zero or more memory blocks writeable by a software package being either predetermined or determined during execution of the software packages in accordance with a set of one or more rules.
 - 46. The apparatus of claim 26 wherein an executive software package enforces the discipline that each software package executes only during the time intervals of its sequence of time intervals, the executive software package determining when the execution of a software

P573C

- package extends into a time interval belonging to the sequence of time intervals assigned to another software package and performs a remedial action.
- The apparatus of claim 26 wherein the presence of those software packages that
- 2 are present is detected.

HILL HAN BAR

FU

ĮŢ

the that that the

- 1 48. The apparatus of claim 26 wherein one or more of the plurality of software
- 2 packages are independently compiled, linked, and loaded.
- 1 49. The apparatus of claim 26 wherein a software package has its own stack, the
- 2 software package's stack being selected prior to executing the software package.

03/27/01 21